



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/032,109	12/20/2001	Erik E. Erlandson	10016854-1	7028

7590 01/23/2006

HEWLETT-PACKARD COMPANY  
Intellectual Property Administration  
P.O. Box 272400  
Fort Collins, CO 80527-2400

EXAMINER

SONG, JASMINE

ART UNIT PAPER NUMBER

2188

DATE MAILED: 01/23/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b> 10/032,109	<b>Applicant(s)</b> ERLANDSON ET AL.	
	<b>Examiner</b> Jasmine Song	<b>Art Unit</b> 2188	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 22 September 2005.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-14, 16-21 and 23-40 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-14, 16-21 and 34-40 is/are allowed.
- 6) ☒ Claim(s) 23-33 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## **Detailed Action**

### **Specification**

1. The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

### **Affidavits, Declaration (37 CFR 1.131)**

2. The declaration filed on 09/22/2005 under 37 CFR 1.131 has been considered and effective to overcome the Fukuyama reference (US application Publication No. 2002/0110037).

### **Claim Rejections - 35 USC § 102**

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 23-26 are rejected under 35 U.S.C. 102(b) as being anticipated by Osawa et al., US 5,946,247.

Regarding claim 23, Osawa teaches that a method, comprising:  
receiving a first address (it is taught as the maximum address value of the maximum address value storage register, col.20, lines 39-40);  
generating a second address (it is taught as the address newly generated in the arithmetic and logic part,col.20, lines 40-41);  
comparing (col.20, lines 39) the first address to the second address; and  
terminating a cycle during which data is being transferred to or from a storage location residing at the second address (it is taught as inhibiting writing of the test pattern in the semiconductor memory; col.20, lines 41-43) if the first address does not have a predetermined relationship to the second address (col.20, lines 43-46).

Regarding claim 25, Osawa teaches that receiving a first address comprises receiving with a memory circuit a first address that is generated outside of the memory circuit (it is taught as receiving the maximum address value of the maximum address value storage register); and generating a second address comprises generating a second address inside of the memory circuit (it is taught as generating the value of the address generation shift register; Fig.32-36, col.45 to col.48).

Regarding claims 24 and 26, Osawa teaches that terminating the cycle comprises terminating the cycle if the first address does not equal the second address

(col.20, lines 41-43); and enabling the cycle if the first address equals the second address (col.20, lines 46--48).

5. Claims 27-33 are rejected under 35 U.S.C. 102(e) as being anticipated by Tanaka., US 6,263,490 B1.

Regarding claim 27, Tanaka teaches that a method, comprising:  
generating a first address (it is taught as generating a drawing address, col.4, lines 16-17);

comparing (Fig.7, comparator circuit 32a) the first address to a predetermined value address (it is taught as a stop address, col.4, lines 23-26);

terminating a cycle during which data is being transferred to or from a storage location residing at the first address (it is taught as generating an interrupt signal which is supplied to the CPU 13) if the first address has a predetermined relationship to the predetermined value (col.5, lines 20-24).

Regarding claim 28, Tanaka teaches that further comprising enabling the cycle if the first address does not have the predetermined relationship to the predetermined value (col.6, lines 37-40).

Regarding claim 29, Tanaka teaches that wherein generating a first address comprises generating the first address inside of a memory circuit (it is taught as generating a drawing address).

Regarding claim 30, Tanaka teaches that wherein generating a first address comprises generating the first address outside of a memory circuit (col.4, lines 36-39).

Regarding claim 31, Tanaka teaches further comprising disabling the cycle comprises disabling the cycle if the first address equals the predetermined value (col.5, lines 20-24); and enabling the cycle if the first address does not equal the predetermined value (col.6, lines 37-40).

Regarding claim 32, Tanaka teaches that further comprising wherein generating a first address comprises generating the first address inside of a memory circuit (it is taught as generating a drawing address); and receiving with the memory circuit a second address from outside of the memory circuit (col.4, lines 36-39).

Regarding claim 33, Tanaka teaches that further comprising loading the predetermined value into a memory that includes the storage location (col.4, lines 21-22).

### **Allowable Subject Matter**

6. Claims 1-14,16-21,34-40 are allowed.

7. When responding to the office action, Applicant is advised to clearly point out the patentable novelty which he or she thinks the claims present in view of the state of the art disclosed by the references cited or the objections made. He or she must also show how the amendments avoid such references or objections. See 37 C.F.R. 1.111 (c).

8. When responding to the office action, Applicants are advised to provide the examiner with the line numbers and page numbers in the application and/or references cited to assist examiner to locate the appropriate paragraphs.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jasmine Song whose telephone number is 571-272-4213. The examiner can normally be reached on 8:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mano Padmanabhan can be reached on 571-272-4210. The fax phone numbers for the organization where this application or proceeding is assigned are 571-273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

Jasmine Song



Patent Examiner

January 17, 2006



Mano Padmanabhan

Supervisory Patent Examiner

Technology Center 2100

**MANO PADMANABHAN**  
**SUPERVISORY PATENT EXAMINER**